

# OSCILLATOR SBXO - 23

LVDS OSCILLATOR  
DIP PACKAGE

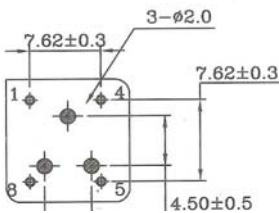
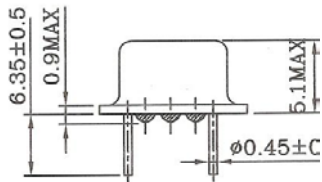
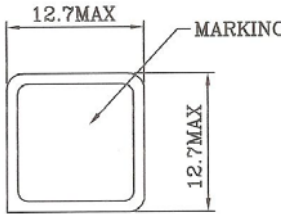
## APPLICATIONS

- ▶ Differential signaling
- ▶ SONET
- ▶ Repeater
- ▶ OC series / STM1

PICTURE  
AREA

### OUTLINE DIMENSIONS

< HALF SIZE / 8 PIN DIP >



Pin no.	Configuration
# 1	N.C or EN/ DIS(tri -stat)
# 4	GND
# 5	Output
# 8	Vcc

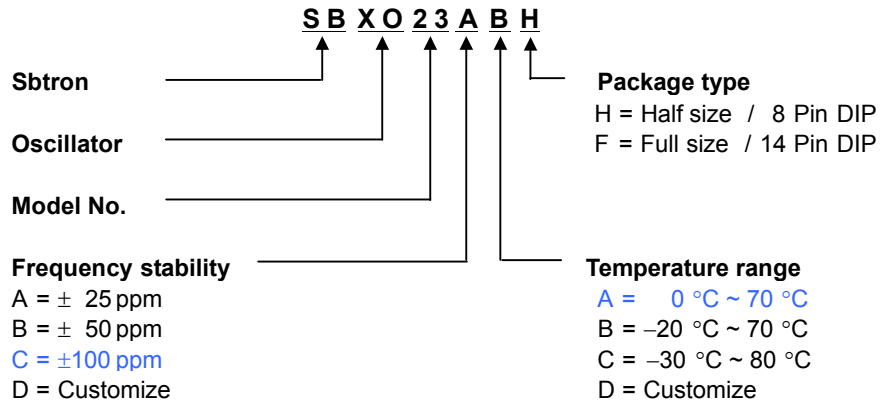
### ELECTRICAL SPECIFICATIONS

Frequency range	700KHz to 800.000MHz		
Frequency stability	±25ppm	±50ppm	±100ppm
	Inclusive of operating temperature		
Operating temperature range	0 °C ~ 70 °C		
Storage temperature range	-50 °C ~ 105 °C		
Input voltage	+3.3Vdc ±0.3V		
Input current	70mA max.		
"0" level	0.90V <sub>DC</sub> max.		
"1" level	1.60V <sub>DC</sub> min.		
Duty ratio	55/45% or 45/55% (at 1.25V <sub>DC</sub> )		
Rise & Fall time	1nS max. (at RL = 100Ω, CL = 10pF)		
Period jitter	9pS max. (In case of 155.520MHz)		
Phase noise relative to carrier (In case of 155.520MHz)	@ 10Hz	-65dBc/Hz	@ 10KHz : -128dBc/Hz
	@ 100Hz	-95dBc/Hz	@ 100KHz : -125dBc/Hz
	@ 1KHz	-114dBc/Hz	
Load drive capability	50Ω		
Aging (at 25 °C)	±5ppm / year max.		

### ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

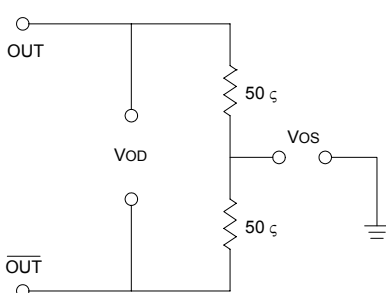
### PART NUMBERING GUIDE



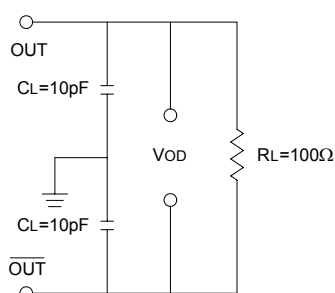
Standard specifications for product indicated in **BLUE** character

Specifications subjects to change without notice & If you need other specifications, Contact our factory.

### LVDS LEVELS TEST CIRCUIT



### LVDS SWITCHING TEST CIRCUIT



### LVDS TRANSITION TIME

