

VCXO SBVX - 15

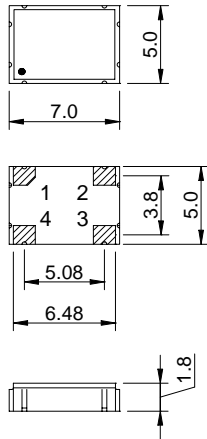
CERAMIC SMD VCXO
7mm X 5mm X 1.8mm(H)

APPLICATIONS

- ▶ Suppress jitter
- ▶ Set top box
- ▶ PLL circuit
- ▶ Potable equipment

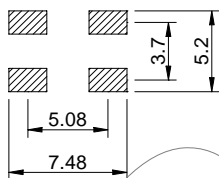
PICTURE
AREA

OUTLINE DIMENSIONS



Pin No.	Function
1	Voltage control
2	GND
3	Frequency output
4	Vcc

< PAD LAYOUT >



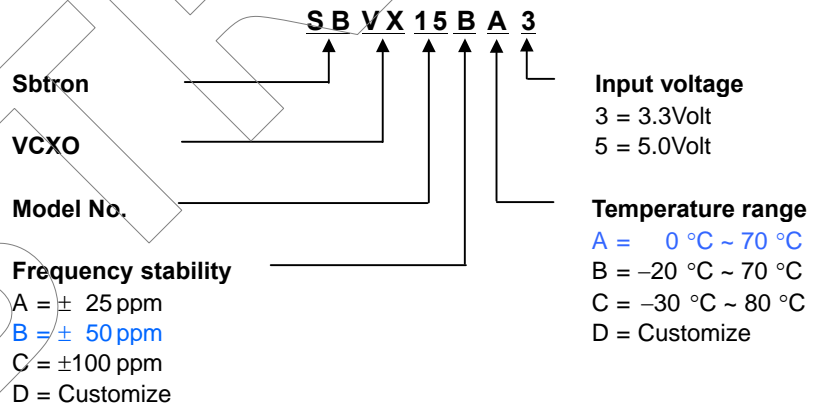
ELECTRICAL SPECIFICATIONS

Frequency range	1.000MHz to 125.000MHz		
Frequency stability	±25ppm	±50ppm	±100ppm
	Inclusive of operating temperature		
Operating temperature range	0 °C ~ 70 °C		
Storage temperature range	-50 °C ~ 105 °C		
Input voltage	+3.3Vdc ±0.3V	+5.0Vdc ±0.5V	
Input current	40mA max.	55mA max.	
"0" / "1" level	+0.4V max. / +2.4V min.	+0.5V max. / +4.5V min.	
Duty ratio	60/40% or 40/60% (typical)		
Pulling range	±100ppm min. (at +1.65Vdc ±1.35V)		
Linearity	10% max.		
Rise & Fall time	5nS max. at +3.3Vdc (+0.4Vdc to +2.4Vdc)		
Start up time	5mS max. (at +3.3Vdc)		
Fan out	15pF		
Aging (at 25 °C)	±5ppm / year max.		

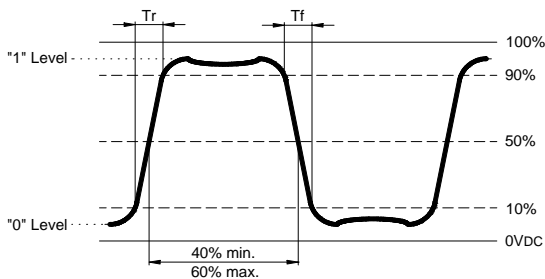
ENVIRONMENTAL & MECHANICAL SPECIFICATIONS

Shock	MIL-STD-883C, Method 2002, Condition B
Vibration	MIL-STD-883C, Method 2007, Condition A
Solderability	MIL-STD-883C, Method 2003
Seal integrity	MIL-STD-883C, Method 1014, Condition C & A2
Marking	MIL-STD-202F, Method 215

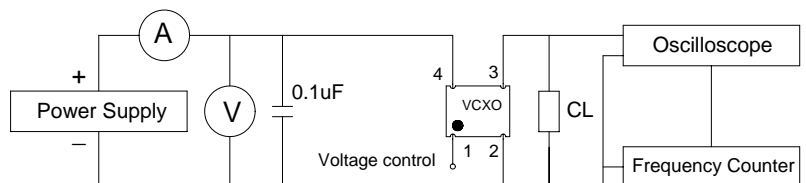
PART NUMBERING GUIDE



WAVE FORM



TEST CIRCUIT



NOTES : (1) CL ... 15pF INCLUDES ALL STRAY AND SCOPE/CTR. LOADING CAPACITANCE